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ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10054373	FILING DATE 01/22/2002	CLASS 361 44	SUBCLASS 224	GAU 3852	EXAMINER F. J. ...
**APPLICANTS: Chen Jiong; Chen Jihliang; Qiao Jianmin; 283					
**CONTINUING DATA VERIFIED: OK					
** FOREIGN APPLICATIONS VERIFIED: OK					
PG-PUB		DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		AIBT 12K09	
Verified and Acknowledged Examiners's initials		ASH			
TITLE : Semiconductor processing chamber substrate holder method and structure					
U.S. DEPT. OF COMMERCE / PAT. & TM. PTO-4261 (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED		
				Total Claims	Print Claim for O.G.	
ISSUE FEE		Primary Examiner	DRAWING			
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.	Print Fig.	
			Application Examiner			
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE				
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